

Fig. 1

Title: DISSIPATING HEAT IN AN ARRAY OF CIRCUIT COMPONENTS

Applicant: JOSEPH J. SPRYSHAK

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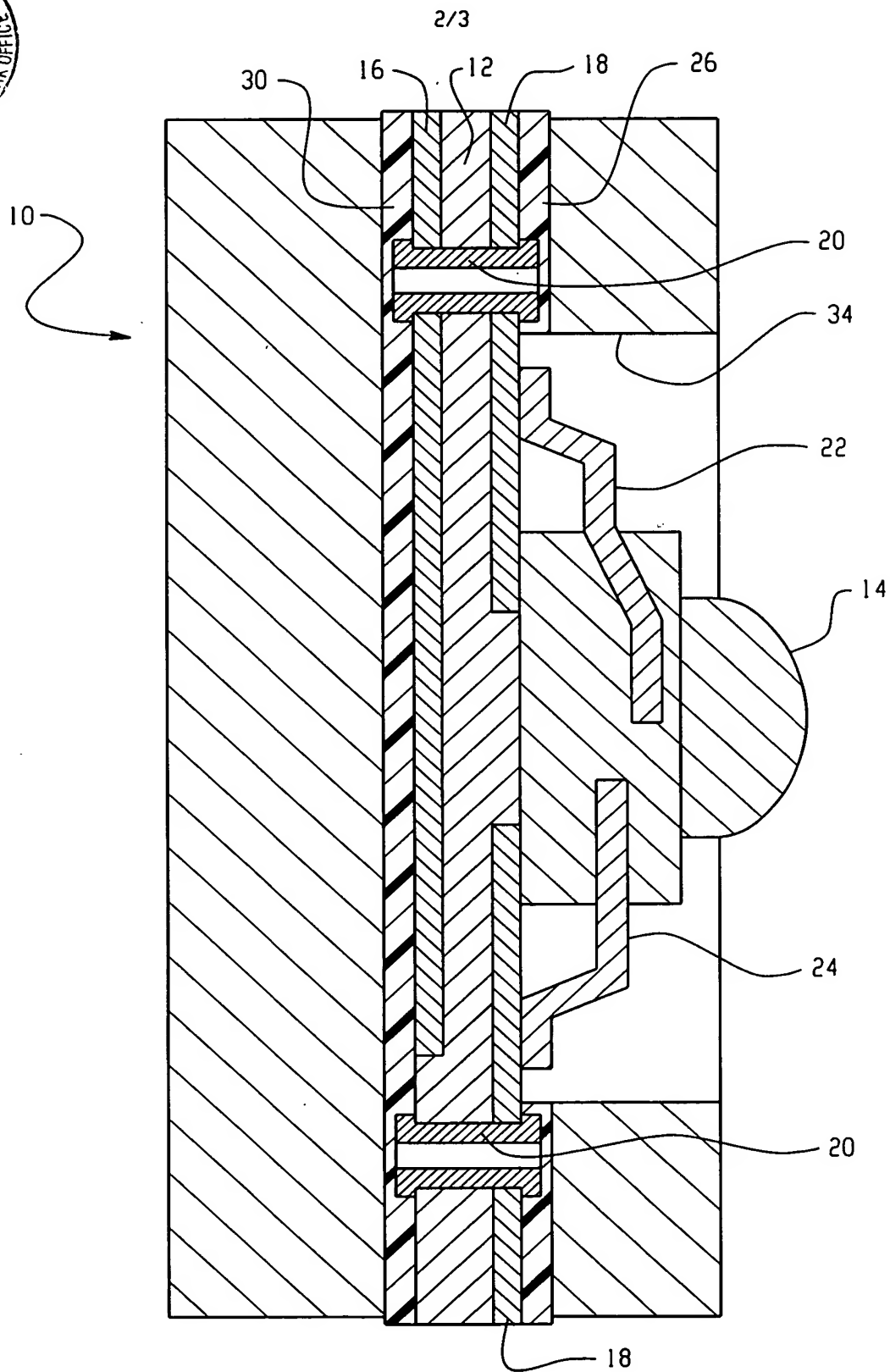


Fig. 2

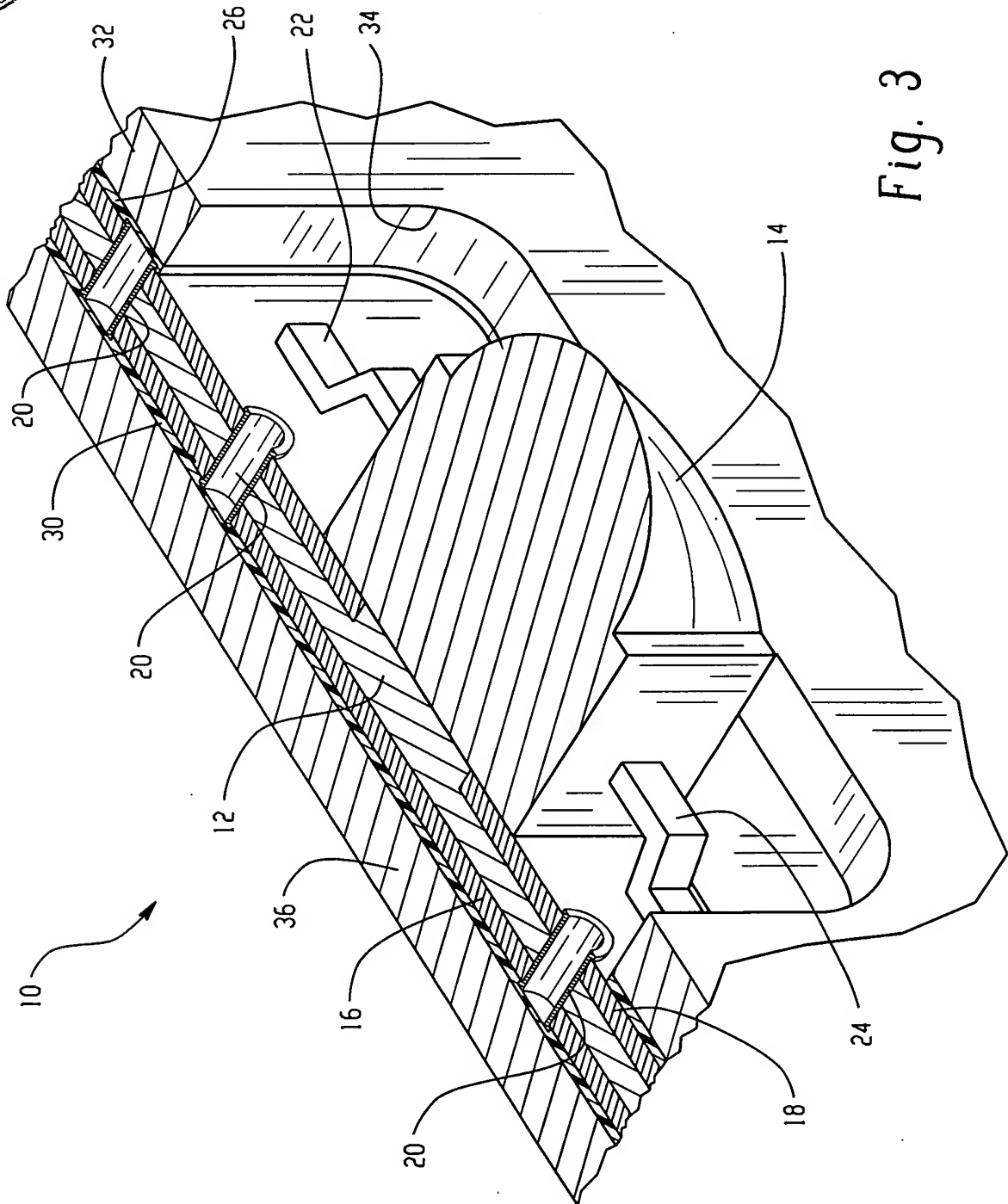
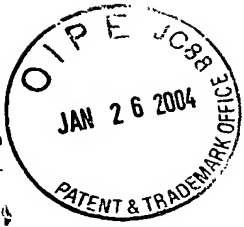


Fig. 3